

How to Write Fast Numerical Code

Fall 2016

Lecture: Balance Principles, Part II

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References

- These slides and the work is from Kenneth Czechowski, Rich Vuduc et al., Georgia Tech
- Kenneth Czechowski, Casey Battaglino, Chris McClanahan, Aparna Chandramowlishwaran, and Richard Vuduc. **Balance principles for algorithm-architecture co-design.** In *Proc. USENIX Wkshp. Hot Topics in Parallelism (HotPar)*, May 2011.
- Kenneth Czechowski, Chris McClanahan, Casey Battaglino, Kartik Iyer, P.-K. Yeung, Richard Vuduc. **On the communication complexity of 3D FFTs and its implications for exascale.** In *Proceedings of the ACM International Conference on Supercomputing (ICS)*, 2012.

Balance Principles II

Czechowski et al. 2011

$$T_{mem} \leq T_{comp}$$

$$\frac{p\pi}{\beta} \left(1 + \frac{\alpha\beta/\lambda}{Q/D} \right) \leq \frac{W}{Q\lambda} \left(1 + \frac{p}{W/D} \right)$$

Application: Analyze Effect of HW Trends

Czechowski et al. 2012

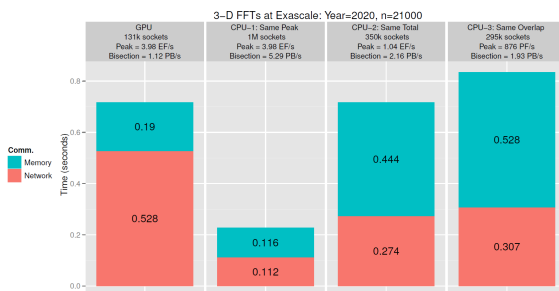
10 year extrapolation (2010 – 2020)

Parameter	2010 values	Doubling time (in years)	10-year increase factor	2020 value
Peak: C _{cpu}	50.4 GF/s	1.7	59.0x	3.0 TF/s
C _{cpu}	515 GF/s			30 TF/s
Cores ^a : p _{cpu}	6	1.87	40.7x	134
	448			18k
Memory bandwidth: β _{cpu}	21.3 GB/s	3.0	9.7x	206 GB/s
β _{cpu}	144 GB/s			1.4 TB/s
Fast memory: Z _{cpu}	6 MB	2.0	32.0x	192 MB
Z _{cpu}	2.7 MB ³			86.4 MB
Line size: L _{cpu}	64 B	10.2	2.0x	128 B
L _{cpu}	128 B			256 B
Link bandwidth: φ _{link}	10 GB/s	2.25	21.8x	218 GB/s
Machine peak: R _{peak}	4 PF/s	1.0	1000x	4 EF/s
System memory: E	635 TB	1.3	208x	132 PB
Nodes: P _{cpu}	79,400	2.4	17.4x	1.3M
(R _{peak} /E): P _{cpu}	7,770			135,000

Application: Analyze Effect of HW Trends

Czechowski et al. 2012

3D-FFT in 2020:
Faster on CPU or GPU?



Application: Analyze Effect of HW Trends

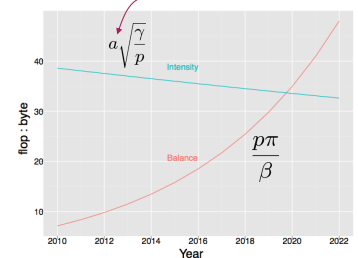
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Matrix-multiplication on GPU

$$\frac{p\pi}{\beta} \leq O\left(\sqrt{\frac{\gamma}{p}}\right)$$



Even Matmult on GPU could become memory bound!

